



IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

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PATENT APPLICATION

INVENTORS:

Thomas J. Krutsick

CASE: 5

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**TITLE:** FIELD PLATED RESISTOR WITH ENHANCED  
ROUTING AREA THEREOVER

**SERIAL NO.** 09/650,604

**GROUP ART UNIT** 2826

**FILING DATE** 04/18/00

**EXAMINER** AHMED N. SEFER

**ASSISTANT COMMISSIONER FOR PATENTS**  
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